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PATENT  
81790.0204

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
  
Natsuki KUSHIYAMA  
  
Serial No: Not assigned  
  
Filed: June 22, 2001  
  
For: SEMICONDUCTOR INTEGRATED CIRCUIT AND  
SEMICONDUCTOR APPARATUS SYSTEM

Art Unit: Not assigned  
  
Examiner: Not assigned

**TRANSMITTAL OF INFORMATION DISCLOSURE  
STATEMENT**

Box PATENT APPLICATION  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: June 22, 2001

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FORM PTO-1449		Docket Number (Optional) 81790.0204	Application Number Not assigned			
INFORMATION DISCLOSURE CITATION IN AN APPLICATION		Applicant  Natsuki KUSHIYAMA				
(Use several sheets if necessary)		Filing Date June 22, 2001	Group Art Unit Not assigned			
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,915,105	6/22/99	Farmwald et al.			
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION (YES/NO)
	7-79149	3/20/95	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
EXAMINER		DATE CONSIDERED				
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.						